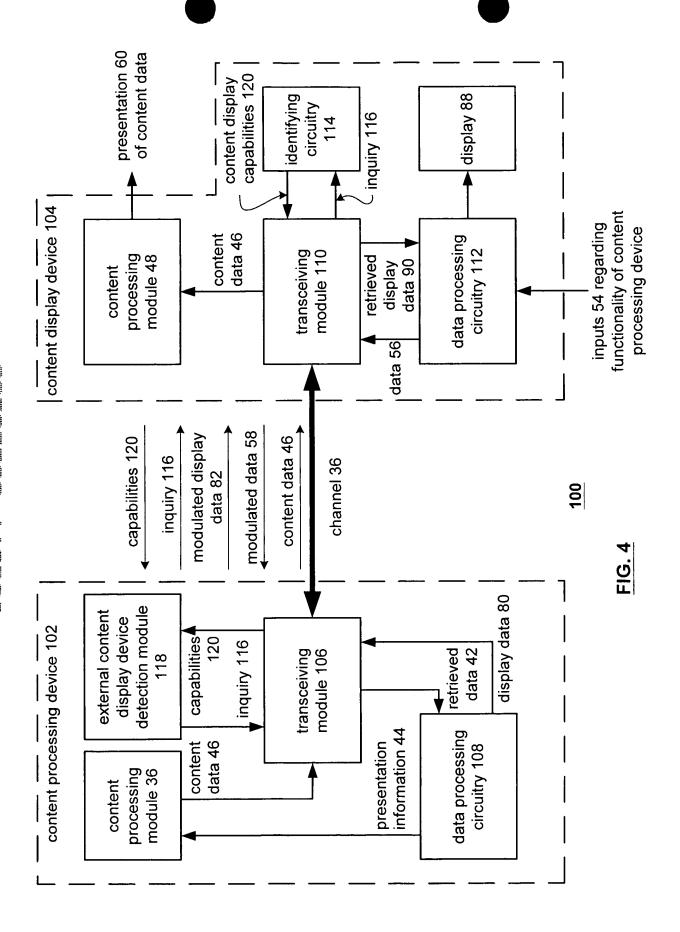
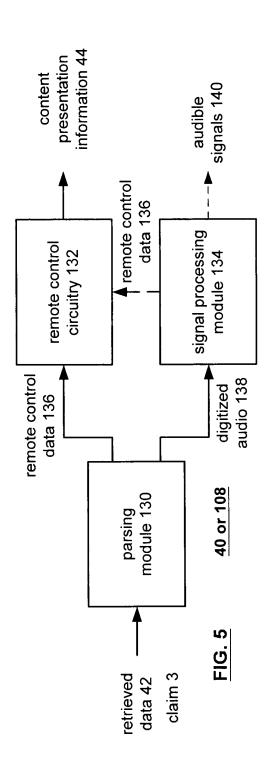
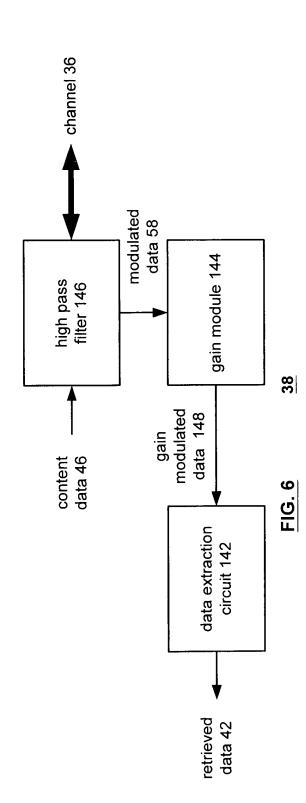
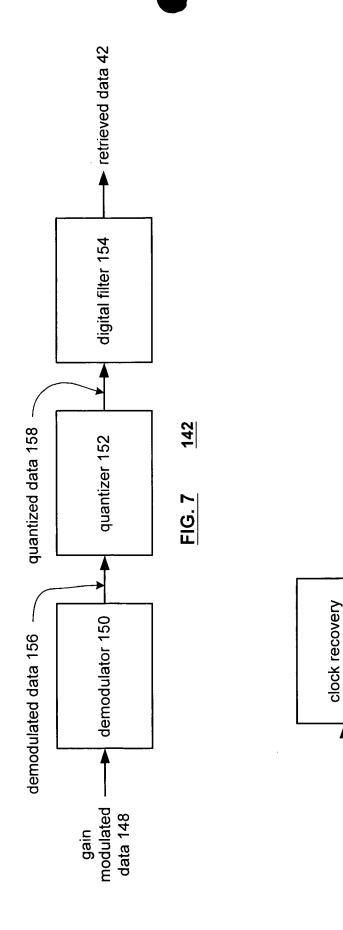


FIG. 3









retrieved data 42

comparator 164

142

FIG. 8

phase

correlated data 168

correlator 160

modulated data 148

gain

signal 166

clock

circuit 162

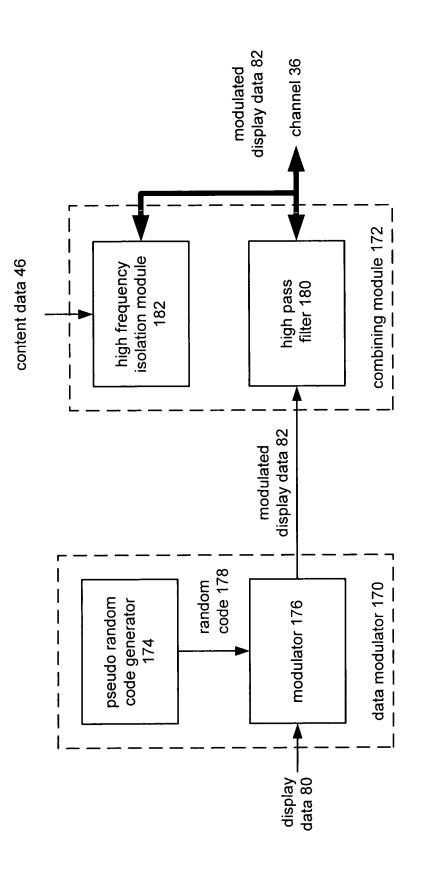
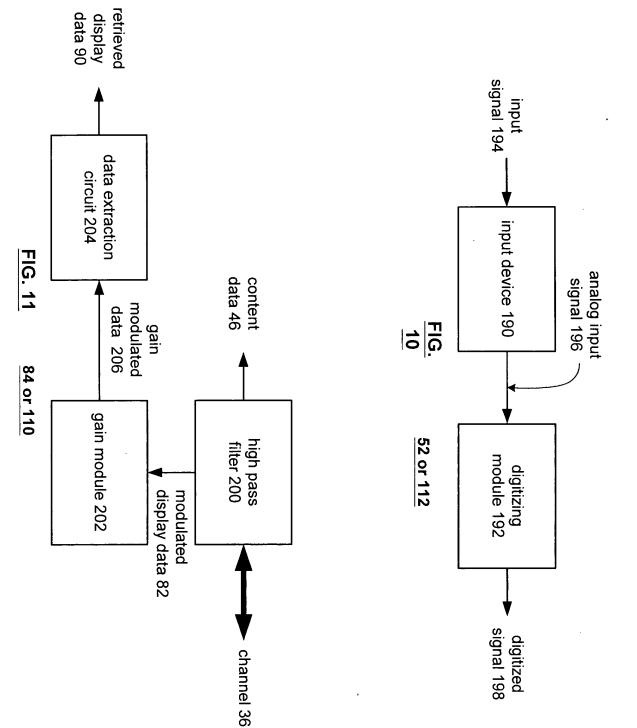
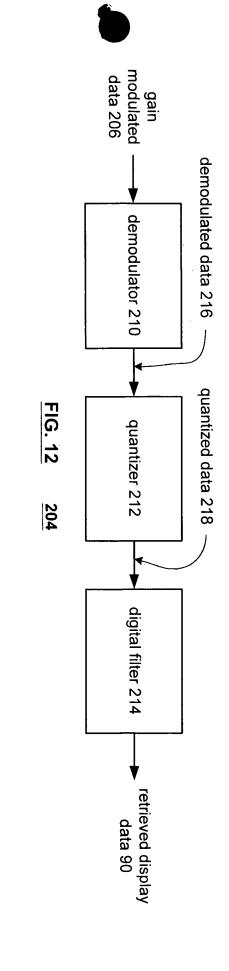
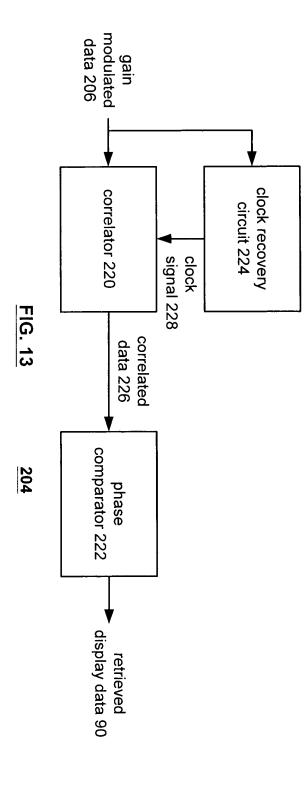


FIG. 9

92







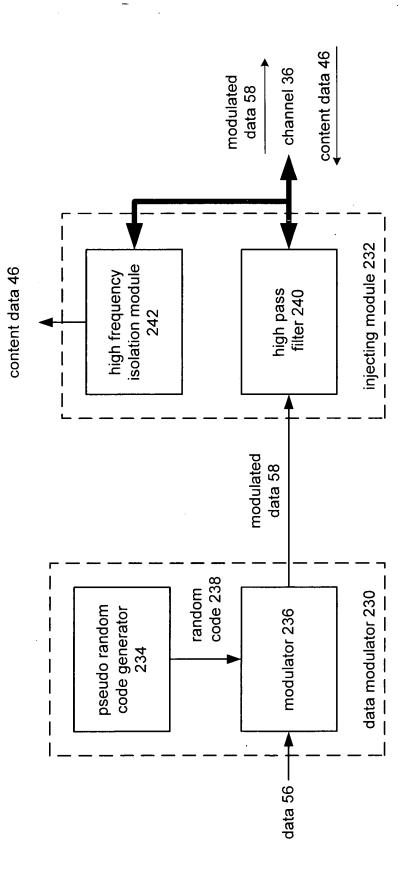
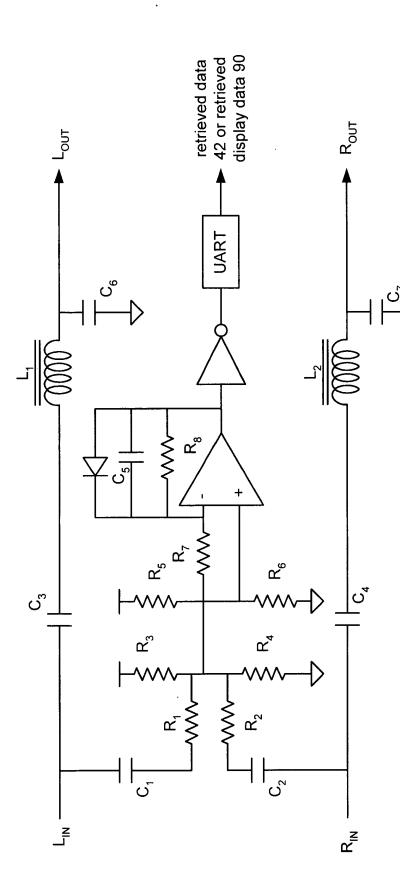


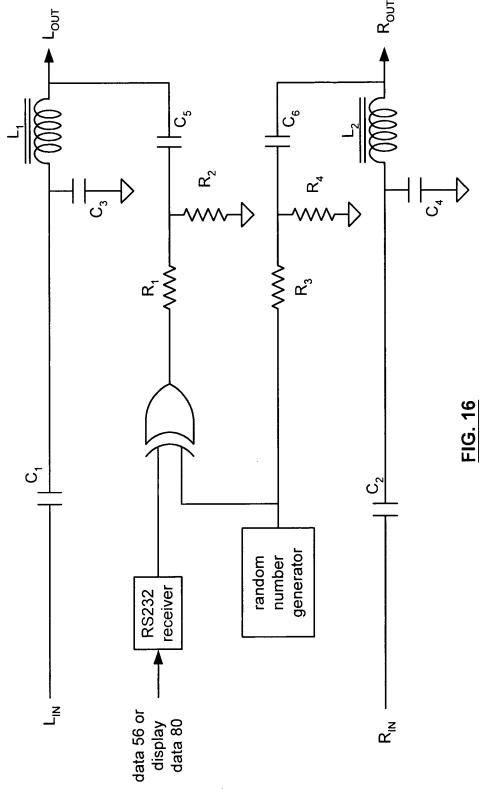
FIG. 14

84

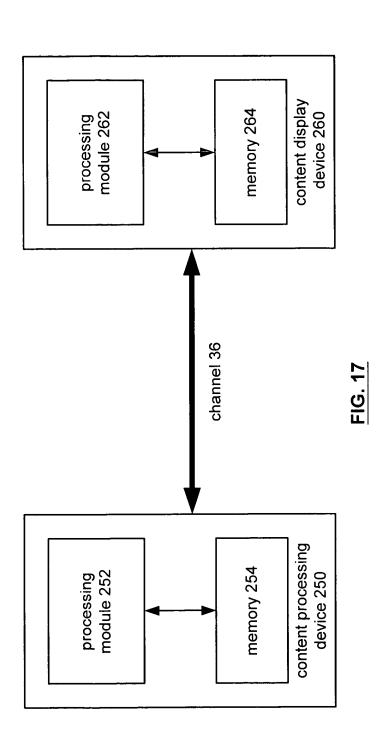


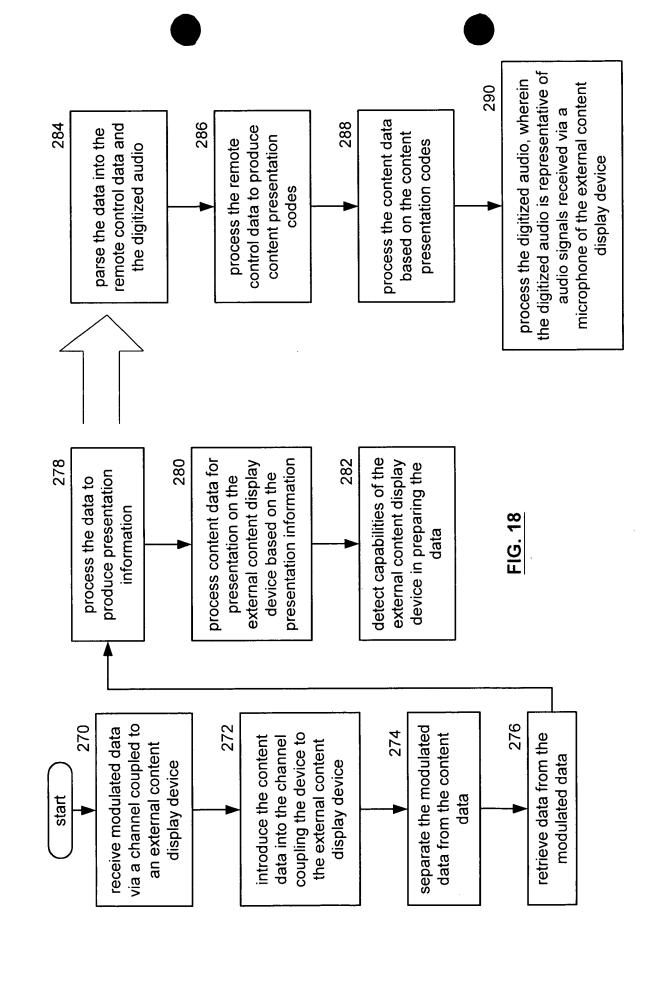
receiving portion of transceiving module 38,50, 76, 84, 106, 110

FIG. 15



transmitting portion of transceiving module 38,50, 76, 84, 106, 110





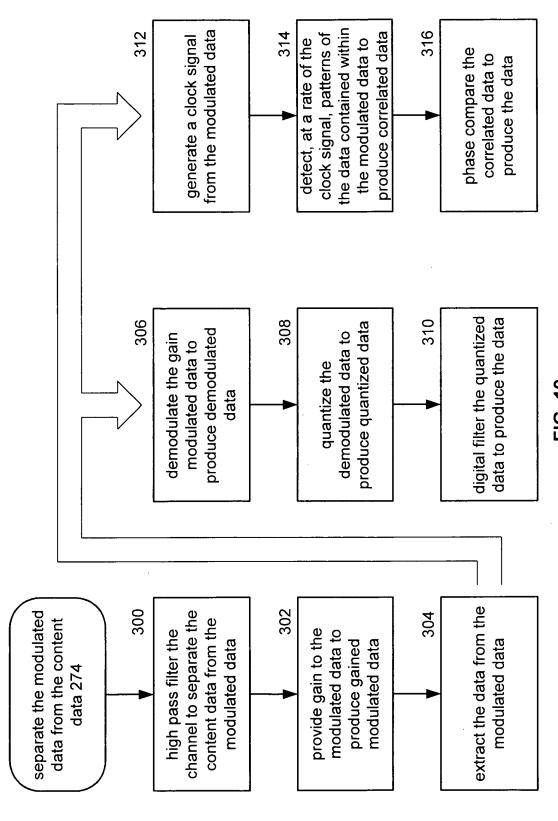
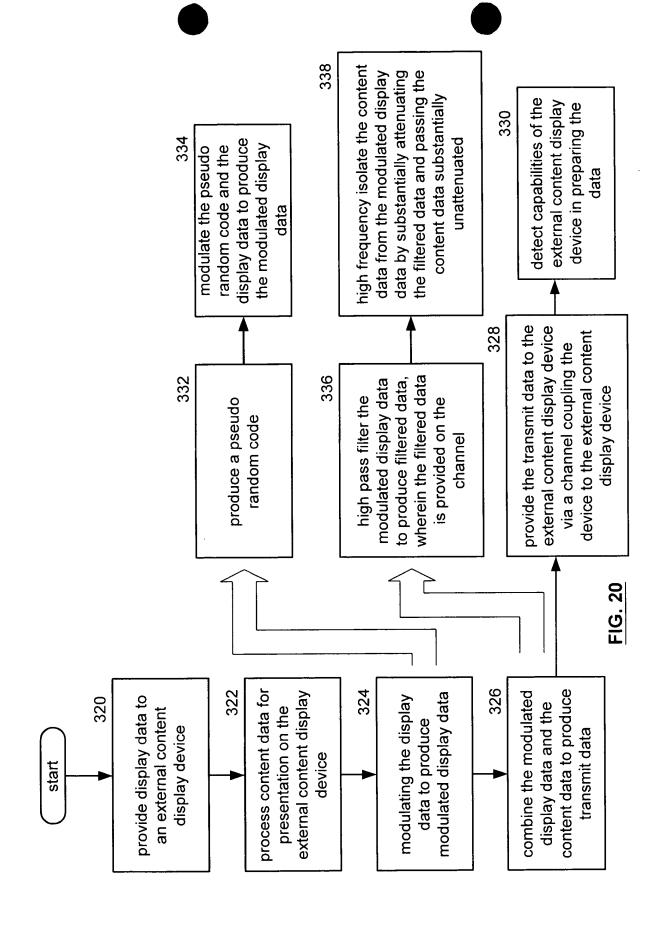
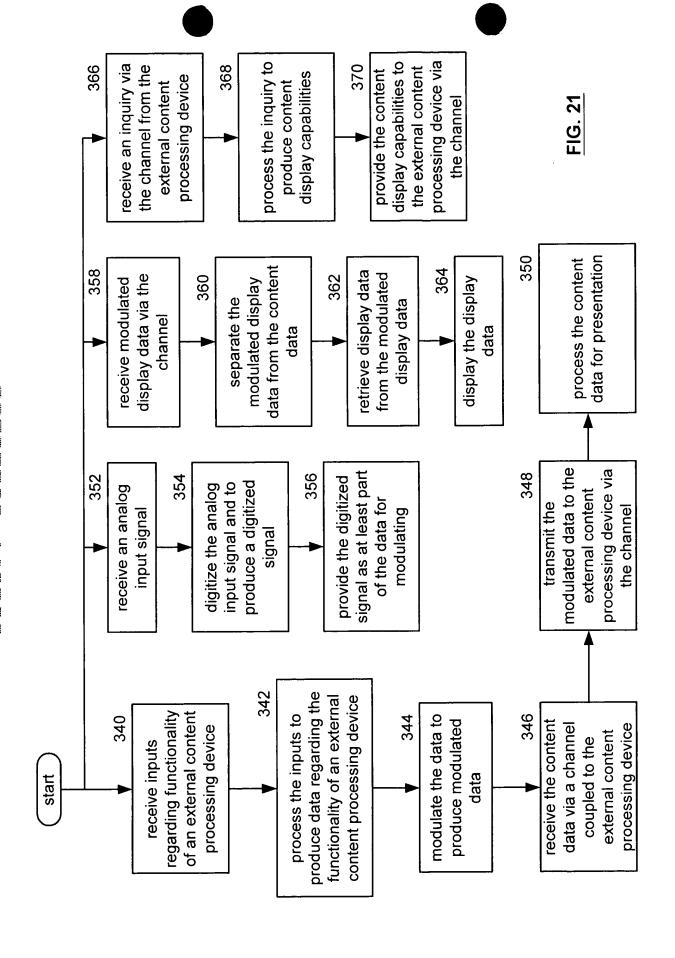


FIG. 19





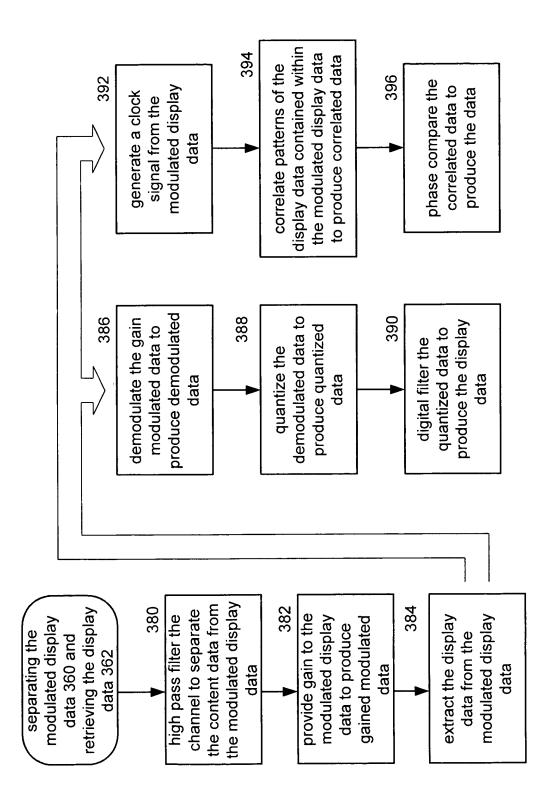


FIG. 22

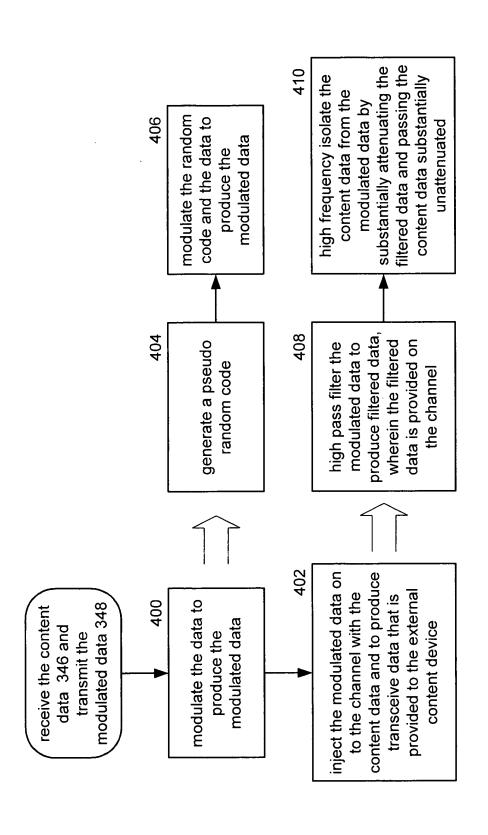
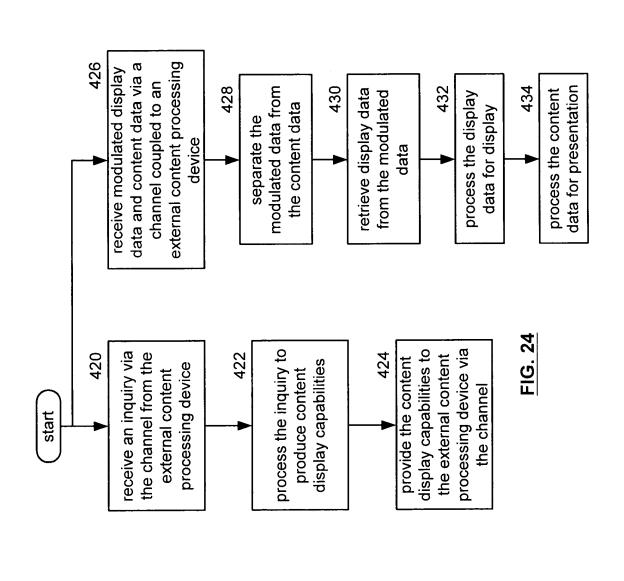


FIG. 23



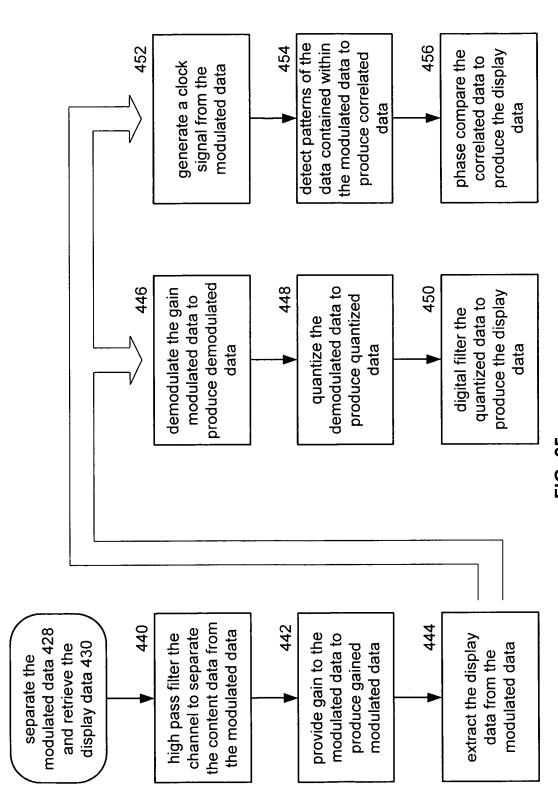


FIG. 25